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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/752,660	12/28/2000	Yutaka Doi	H0001384 (4970)	9390

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EXAMINER

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ART UNIT PAPER NUMBER

2827

DATE MAILED: 04/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.



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☐ The file on the cover sheet cannot be located after a reasonable search. Therefore, the Office is initiating the reconstruction of the file of the above-identified application pursuant to the provisions of 37 CFR 1.251.

Applicant is hereby requested to provide a copy of applicant's record (if any) of all of the correspondence between the Office and applicant for the above-identified application (except for U.S. patent documents), a list of such correspondence, and a statement that the copy is a complete and accurate copy of applicant's record of all of the correspondence between the Office and the applicant for the above-identified application (except for U.S. patent documents), and whether applicant is aware of any correspondence between the Office and applicant for the above-identified application that is not among applicant's records.

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In re Application of: YUTAKA DOI
Application No.: 09/752660
Filing Date: 12/28/2000
Title: LAYERED CIRCUIT BOARDS AND METHODS OF PRODUCTION THEREOF
Direct to: U. S. Patent and Trademark Office
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☐ The copy of the paper(s) listed in the notice under 37 CFR 1.251 is/are a complete and accurate copy of applicant's record of such paper(s).

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☐ Applicant does not possess any record of the correspondence between the Office and the applicant for the above identified application.

Date

Signature

Typed or printed name

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